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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *Abm*

This application is a DIV of 10/317,481 12/12/2002 PAT 6,710,436

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *none* *Abm*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>Abm</i> Examiner's Signature Initials	CA	5	16	1

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## TITLE

Method and apparatus for electrostatically aligning integrated circuits

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